

### Silicon Power Diode

**PSM/PSMR 40L**  
**PSM/PSMR 40K**

$I_{F(AV)} = 40 \text{ A}$   
 $V_{RRM} = 100 - 1600 \text{ V}$

Preliminary Data Sheet

$V_{RRM}$ max. repetitive peak voltage (V)	$V_{R(RMS)}$ max. RMS reverse voltage (V)	$V_R$ max. DC blocking voltage (V)	recommended RMS working voltage (V)	Type	
				without terminal lead	with terminal lead
100	70	100	40	PSM/PSMR 40/01L	PSM/PSMR 40/01K
200	140	200	80	PSM/PSMR 40/02L	PSM/PSMR 40/02K
400	280	400	160	PSM/PSMR 40/04L	PSM/PSMR 40/04K
600	420	600	240	PSM/PSMR 40/06L	PSM/PSMR 40/06K
800	560	800	320	PSM/PSMR 40/08L	PSM/PSMR 40/08K
1000	700	1000	400	PSM/PSMR 40/10L	PSM/PSMR 40/10K
1200	840	1200	480	PSM/PSMR 40/12L	PSM/PSMR 40/12K
1400	980	1400	560	PSM/PSMR 40/14L	PSM/PSMR 40/14K
1600	1120	1600	640	PSM/PSMR 40/16L	PSM/PSMR 40/16K

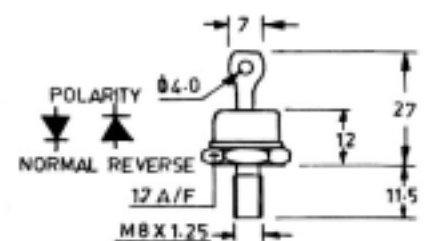
Symbol	Conditions	Maximum Ratings	
$I_{F(AV)}$	$T_C = 140^\circ\text{C}$	40	A
$I_{FSM}$	$T_{VJ} = 45^\circ\text{C}$ $t = 10 \text{ ms}$	500	A
$I_{FRM}$	max. peak cycle repetitive surge current	200	A
$I^2t$	max. $I^2t$ rating (non-rep.) for 5 to 10 ms	1200	A <sup>2</sup> s
$I_{R(AV)}$	max. average reverse leakage current at $V_{RRM}$ ; $T_C = 25^\circ\text{C}$	200	$\mu\text{A}$
$V_{FM}$	max. peak forward voltage drop @ rated $I_{F(AV)}$	1.3	V
$R_{thJC}$	max. thermal resistance junction to case	0.65	K/W
$T_{VJ}$	operating junction temperature	-65... + 150	$^\circ\text{C}$
$T_{VJM}$	max. virtual junction temperature	150	$^\circ\text{C}$
$T_{stg}$	storage temperature	-65... + 200	$^\circ\text{C}$
$M_d$	mounting torque	min. 0.4	mkg
	(non-lubricated threads)	max. 0.6	mkg
<b>Weight</b>	PSM/PSMR 40L	typ. 13.5	g
<b>Weight</b>	PSM/PSMR 40K	typ. 30	g

### Features

- All Diffused Series
- Available in Normal & Reverse Polarity
- Industrial Grade
- Available in Avalanche Characteristic

### DO - 5

PSM/PSMR 40L



PSM/PSMR 40K

